



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Ronald D. McCallister

Serial No.: 10/766,779

Group Art Unit: 2611

Filed: 27 January 2004

Examiner: BAYARD, Emmanuel

For: PREDISTORTION CIRCUIT AND METHOD FOR COMPENSATING NONLINEAR DISTORTION IN A  
DIGITAL RF COMMUNICATIONS TRANSMITTER

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Sir:

In accordance with 37 C.F.R. § 1.56, the references listed on the attached form PTO/SB/08B are being brought to the attention of the Examiner for consideration in connection with the examination of the above-identified patent application. Copies of the cited foreign patents and non-patent literature are enclosed.

This information is being submitted subsequent to the later of three months after the filing date of the present application and subsequent to the first mailing of the first Office Action on the merits. The fees necessitated by this Supplemental Information Disclosure Statement (\$180.00) are enclosed herewith by way of credit-card authorization form.

It is respectfully requested that the Examiner indicate consideration of the cited references by returning a copy of the attached form PTO/SB/08B with initials or other appropriate marks.

Respectfully submitted,

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5 February 2008  
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